

Features

- V_{IN} Range 1.7 – 12V
- Fixed and adjustable output voltage
- 500mA maximum output current
- 300mV typical dropout voltage
- Bypass output for low PSRR and output noise
- Power Good pin with 500 μ s delay
- Low self noise
- Shutdown mode for longer battery life
- High PSRR typically 60dB at 100Hz
- Stable with a wide range of Ceramic, Tantalum or Electrolytic capacitors
- Current limit protection
- Thermal Shutdown
- -40°C to 125°C temperature range
- Available in SOT23-5

Applications

- Set Top Boxes
- Digital Audio systems
- DSP
- POL regulators
- SMPS post regulator

Description

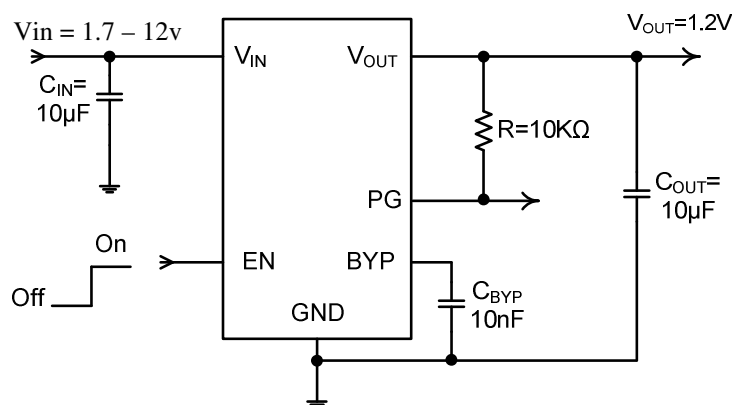
The PS22921 is a high performance, precision linear regulator with very low dropout voltage and excellent transient response. It is designed to operate with wide input voltage range of 1.7 – 12 Volts making it ideal for two step conversion. The device is capable of supplying 500mA of output current with a typical dropout voltage of 300mV. The output Voltage is available in either fixed or adjustable options. For fixed option, the internal resistor network can be set in 100mV increment ranging from 0.5 to 5.0 volts.

The product includes an Enable pin for electrical on/off of the regulator. Forcing the Enable pin to logic low shuts down the LDO and reduces the supply current below 1 μ A. The PS22921 features fast transient response and low self output noise. Connecting a capacitor from ByPass to GND further enhances the PSRR and the output noise.

An open collector power good flag with 500 μ s delay is available for systems requiring supply monitoring and sequencing of other supplies. The regulator features the full suite of protection with a combination of current limit and thermal overload protection.

The PS22921 is available in SOT23-5 and it is rated for -40°C to +125°C junction temperature range.

Typical Application



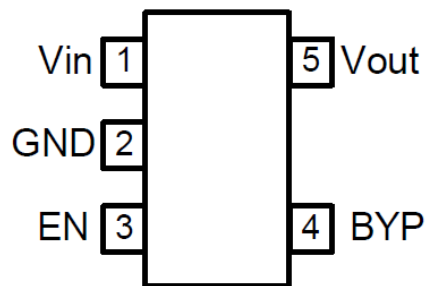
Pin Description

Symbol	Pin # SOT23-5	Description
EN	3	Enable pin. It controls the electrical on/off of the device. When connected to logic low, the device shuts off and consumes less than 1µA of current. Logic high will resume normal operation.
ByPass	4	Bypass pin. Connect an external capacitor from BYP to ground to bypass the noise generated by the internal bandgap. This improves power supply rejection ratio and output noise. A minimum capacitor of 220pF is recommended.
V _{IN}	1	Input supply pin. Connect a 10µF capacitor between this pin and ground.
NC	N/A	No connect.
FB	N/A	Feedback Voltage. A resistor network of two resistors is used to set-up the output voltage connected between V _{out} and GND. The center tap of the two resistors is connected to FB pin. For fixed voltage options, the resistors are internally connected to V _{OUT} .
V _{OUT}	5	Regulated output Voltage. Connect a 10µF capacitor from this pin to ground.
PG	N/A	Power good. It is an open collector, active high output that indicates the status of the output voltage. When output voltage (V _{OUT}) exceeds the PG trip threshold, the PG pin goes in to a high impedance state. When the device is out of regulation or shutdown, the PG pin is pulled low. A 10KΩ to 1MΩ resistor should be connected from PG to V _{OUT} .
GND	2	Ground connection.
	N/A	Expose pad. Connected to PCB ground plane for good thermal dissipation.

NB: N/A = Not available in the shown package type

Pin Configuration

SOT23-5 (Top View)



Absolute Maximum Ratings⁽¹⁾

Maximum Input Supply Voltage.....	-0.8 to 15V
Enable Voltage.....	-0.8 to 15V
Power Good.....	-0.8 to 15V
Feedback Voltage.....	-0.8 to 5V
ByPass Voltage.....	-0.8 to 5V

Recommended Operating Conditions

Input Voltage.....	1.7 to 12V
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Enable input Voltage.....	0V to V_{IN}
Junction operating Temperature.....	-40°C to 125°C

Thermal Information⁽²⁾

SOT23-5 ¹ θ_{JA}	220°C/W
SOIC-8 ¹ θ_{JA}	36°C/W
Storage Temperature Range.....	-65 to 150°C
Lead Temperature.....	260°C
Junction Temperature.....	-40 to 125°C

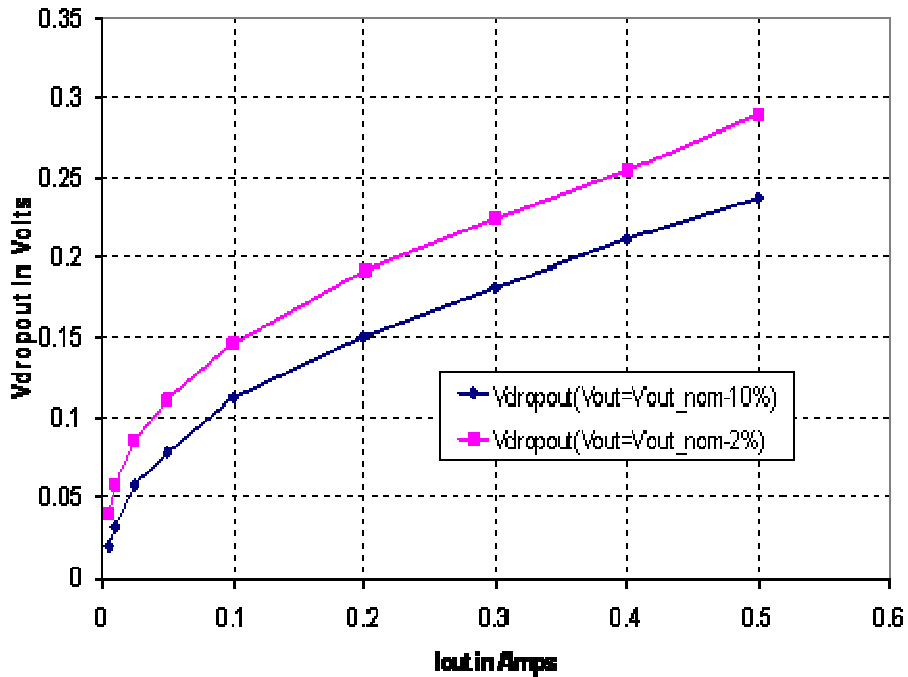
ELECTRICAL CHARACTERISTICS

Unless otherwise noted: $V_{IN}=5V$; $V_O=3.3V$; $C_{IN}=C_{OUT}=22\mu F$; Typical values are $T_A=25^\circ C$

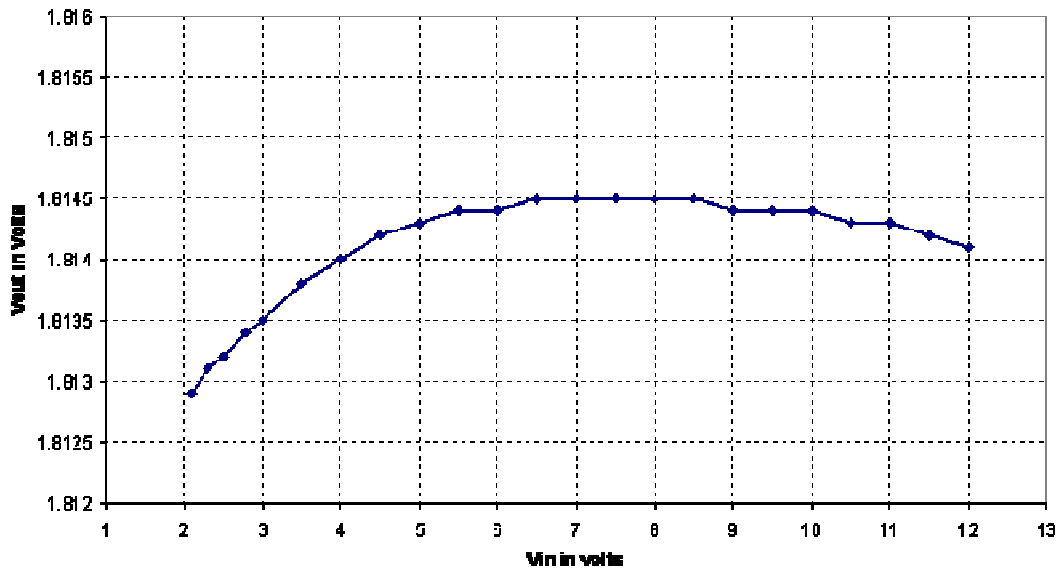
Parameter	Symbol	Conditions	Min.	Typ.	Max.	Units
Output Voltage Range	V_O	Fixed option; 100mV increment	0.5		5.0	V
		Adjustable	0.5		$V_{in} - V_{DO}$	
Output Voltage Tolerance				TBD		%
Feedback Voltage	V_{FB}		0.494	0.50	0.506	V
Maximum Output Current	I_O		500			mA
Shutdown current	I_{SHD}	$V_{EN}=GND$		0.1	1.0	μA
Current Limit	I_{LIM}		550		800	mA
Supply Current	I_Q	$I_{OUT}=100mA$			TBD	mA
		$I_{OUT}=250mA$			TBD	
		$I_{OUT}=500mA$			TBD	
Load Regulation ³		$I_{OUT}=0.1mA - 150mA$	-1.0		1.0	%
Line Regulation		$V_{IN}=V_O+1V$ to 12V; $I_{OUT}=10mA$	-0.5		0.5	%
Dropout Voltage	V_{DO}	$I_{OUT}=100mA$				mV
		$I_{OUT}=250mA$				
		$I_{OUT}=500mA$		300		
Enable Threshold Low	$V_{EN(L)}$				0.8	V
Enable Threshold High	$V_{EN(H)}$		1.4			V
Input Enable Low Current	$I_{EN(L)}$	$V_{EN}=0V$		0.01	2.0	μA
Input Enable High Current	$I_{EN(H)}$	$V_{EN}=2V$		5.0	8.0	μA
Ripple Rejection Ratio	PSRR	$I_O=50mA$	Freq=100Hz		62	dB
			Freq=10KHz		60	
Output Noise	e_n	BW; 10Hz – 100KHz $I_O=100mA$ $C_{OUT}=10\mu F$	$C_{BYP}=Open$		TBD	nV/ \sqrt{Hz}
			$C_{BYP}=100nF$		TBD	
Power good low threshold	V_{OL_PG}	$I_{PG}=200\mu A$			400	mV
Power good delay	V_{PG_delay}			500		μs
Thermal Shutdown	T_{SD}			140		$^\circ C$
Thermal Shutdown Hys.	T_{SD_HYS}			15		$^\circ C$

Notes:

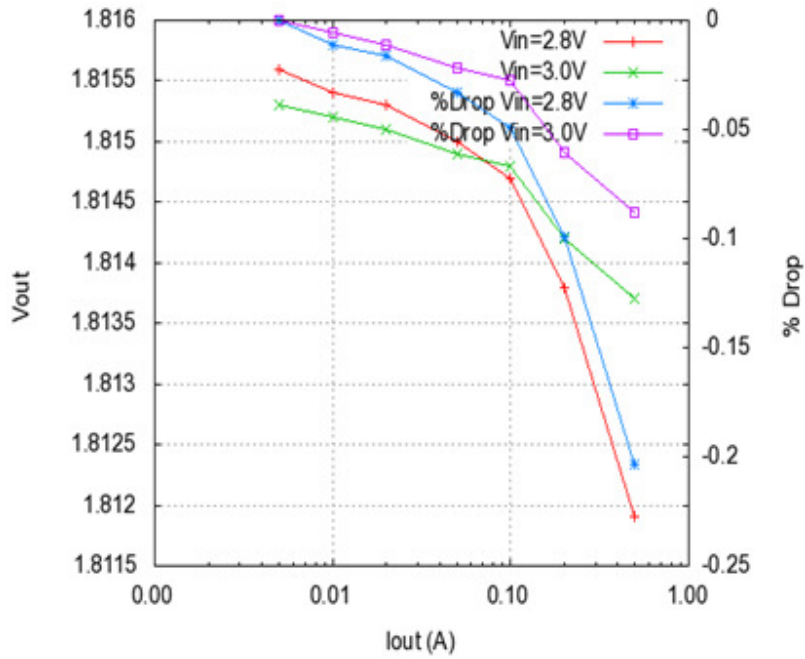
- Stresses above those listed in Absolute Maximum Ratings may cause permanent damage to the device. Measured on approximately 1" square of 1oz copper.
- The PS22921 is guaranteed to meet performance specifications over the -40°C to +125°C operating junction temperature range and is assured by design, characterization, and correlation with statistical process control.
- Load regulation is measured using pulse techniques with duty cycle <5%.



Typical Vdropout vs Iout (A)

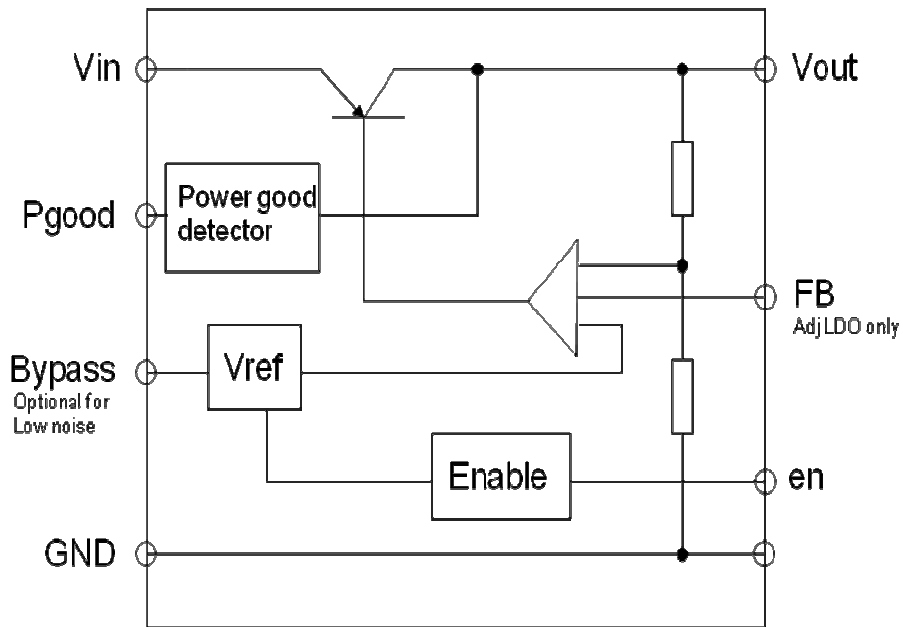


Typical Line regulation vs Vin (V)



Typical Load regulation vs Iout(A)

FUNCTIONAL BLOCK DIAGRAM



APPLICATION HINT

Input Capacitor (C_{IN})

Input capacitor may be required when the device is not near the source power supply or when supplied by a battery. This capacitor will reduce the circuit's sensitivity when powered from complex source impedance and significantly enhance the output transient response. The input bypass should be mounted with the short possible track directly across the regulator's input and ground terminals. A 10µF ceramic capacitor should be adequate for most applications.

Output Capacitor (C_{OUT})

The output capacitor provides not only stability to the regulator, but also, enhances the load transient response. A minimum capacitance of 10µF is required. When selecting a ceramic capacitor, only X5R and X7R dielectric types should be used. Other types such as Z5U and Y5F have such severe loss of capacitance due to effects of temperature variation and applied voltage, they may provide as little as 20% of rated capacitance in many typical applications.

Always consult capacitor manufacturer's data curves before selecting a capacitor. High-quality ceramic capacitors can be obtained from Taiyo-Yuden, AVX, and Murata. Higher values of the output capacitance can be used to enhance loop stability and transient response.

Output Voltage

The adjustable output voltage option allows the user to select an output voltage by using an external resistor divider. PS22921 uses a 0.5V reference voltage at the positive terminal of the error amplifier. To set the output voltage a programming resistor from the feedback pin (FB) to ground must be selected. A 10kΩ resistor is a good selection for a programming resistor R2. A higher value may result in an excessively sensitive feedback node while a lower value will draw more current and degrade the light load efficiency. The equation for selecting the voltage specific resistor is:

$$V_O = \left(1 + \frac{R1}{R2}\right) * V_{FB}$$

For the fixed output devices, R1 and R2 are included within the device.

ByPass Capacitor

Connecting a capacitor between the BYP pin and ground can significantly reduce output noise. Values can range from 0pF to 100nF, depending on the sensitivity to output noise in the application. The start up speed of the PS2291 is inversely proportional to the size of the bypass capacitor. Applications requiring a slow ramp-up of output voltage should consider larger values of bypass capacitance. Likewise, if rapid turn on is necessary, consider omitting C_{BYP}.

Enable/Shutdown

The enable (EN) pin is active high and is compatible with standard digital signalling levels. When V_{EN} below 0.8V, it turns the regulator off while V_{EN} above 1.4V turns the regulator on. If not used, EN can be connected to the input Voltage. If EN is connected to V_{IN}, it should be connected as close as possible to the largest capacitance on the input to prevent voltage droops on that line from triggering the enable circuit.

Thermal consideration

The PS2291 is designed to provide 500mA of continuous current in a very small package. Maximum power dissipation can be calculated based on the output current and the voltage drop across the part. To determine the maximum power dissipation of the package, use the junction-to-ambient thermal resistance of the device and the following basic equation:

$$P_{D(max)} = \left(\frac{T_{J(max)} - T_A}{\theta_{JA}}\right)$$

Where T_{J(max)} is the maximum junction temperature of the die, T_A is the ambient operating temperature, and θ_{JA} is layout dependent. The actual power dissipation of the regulator circuit can be determined using the equation:

$$P_D = (V_{IN} - V_{OUT}) * I_{OUT} + V_{IN} * I_{SUP}$$

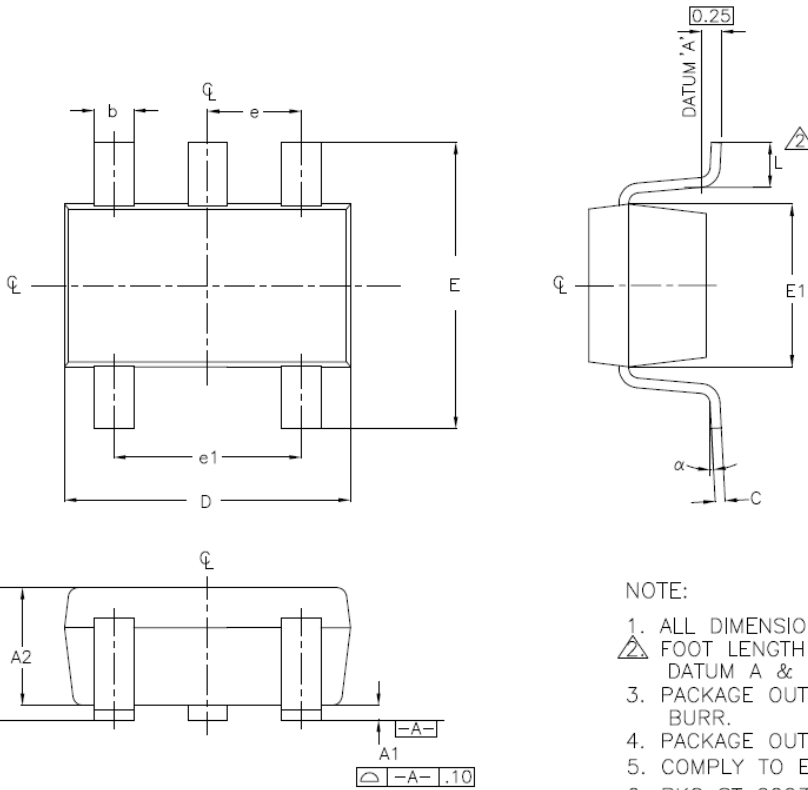
Substituting PD(max) for PD and solving for the operating conditions that are critical to the application will give the maximum operating conditions for the regulator.

Ordering Information

Device	Operating Junction Temp.	Package	Comments
PS22921TET1A5	-40°C to +125°C	SOT23-5	V _o = 1.8v
PS22921T8T1A5	-40°C to +125°C	SOT23-5	V _o = 1.2v
PS22921TUT1A5	-40°C to +125°C	SOT23-5	V _o = 3.3v

N.B For other values of output voltage please contact your local Plessey Semiconductors sales representative

Outline Drawing and Landing Pattern (SOT23-5)




SYMBOL	MIN	MAX
A	0.90	1.45
A1	0.00	0.15
A2	0.90	1.30
b	0.30	0.50
C	0.09	0.20
D	2.80	3.05
E	2.60	3.00
E1	1.50	1.75
L	0.30	0.55
e	0.95ref	
e1	1.90ref	
α	0°	8°

APPROVAL	SIGN	DATE
ENGG MGR		
TOOLING MGR		
OPERATION MGR		

NOTE: DOCUMENT NOT CONTROLLED UNLESS IT HAS RED DOC. CONTROL STAMP.

NOTE:

1. ALL DIMENSIONS ARE IN MILLIMETERS.
2.  FOOT LENGTH MEASURED AT INTERCEPT POINT BETWEEN DATUM A & LEAD SURFACE.
3. PACKAGE OUTLINE EXCLUSIVE OF MOLD FLASH & METAL BURR.
4. PACKAGE OUTLINE INCLUSIVE OF SOLDER PLATING.
5. COMPLY TO EIAJ SC74
6. PKG ST 0003 REV A SUPERCEDES SOT23-D-2005 REV (

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